

Image

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PATENT
09/997,786

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Duncan M. Walker, et al.
Serial No.: 09/997,786 ✓
Filing Date: November 30, 2001
Art Unit: 4798
Title: *SYSTEM AND METHOD FOR DETECTING QUIESCENT
CURRENT IN AN INTEGRATED CIRCUIT*

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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Willie Jiles
Willie Jiles

**PRELIMINARY AMENDMENT AND
RESPONSE TO RESTRICTION REQUIREMENT**

Prior to examination of this utility patent application entitled *System and Method for
Detecting Quiescent Current in an Integrated Circuit*, please amend the application as follows: